



Modelling of an infrared halogen lamp in a rapid thermal system

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ABSTRACT

The heat flux distribution of an infrared halogen lamp in a Rapid Thermal Processing (RTP) equipment is studied. An overview of lamp modelling in RTP systems is given and for the first time, the infrared lamp bank is modelled by taking into consideration with accuracy a lamp portion in the bank environment. A three-dimensional (3D) lamp model, with a fine filament representation is largely presented. The model assumptions are in particular exposed with focusing on the thermal boundary conditions. The lamp temperature is calculated by solving the radiative heat transfer equation by means of the Monte-Carlo method for ray tracing. Numerical calculations are performed with the finite volume method. A very good agreement is found with experimental data in steady state. The heat amount provided by the lamp is also determined. As a first development, transient calculations are performed with the validated model and the dynamic behaviour of the lamp during heating process is determined with precision. Lastly, the model is discussed and further developments are proposed.

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1. Introduction

Rapid Thermal Processes (RTP) are essential in the manufacturing of semiconductor devices such as integrated circuits, memories or solar cells. They correspond to key stages in the wafer production operations like annealing (RTA), oxidation (RTO) or Chemical Vapour Deposition (RTCVD) [1–3]. As feature size decreases towards the nanometre scale and wafer diameter increases, a deep knowledge of the phenomena involved in the processes is crucial. Indeed, there is a growing demand from rapid thermal equipment manufacturers and users to improve control, uniformity and repeatability of wafer processes. As wafer temperature requirement is especially moving to a drastic 1 K precision, wafer heating has to be mastered with high accuracy. Development of numerical tools has accompanied with success the evolution of RTP over the last two decades. Numerical tools have allowed a better understanding of the various aspects of the processes such as wafer heating, gas flow, thin film deposition, system control etc. Heat and mass transfer have been namely simulated by using the Computational Fluid Dynamics (CFD) method in an efficacious way [4,5].

In RTP systems, a silicon wafer is heated up at a very high rate by the radiative heat provided by halogen infrared lamps (Fig. 1a). Process times vary from a few seconds for implant annealing up to

a few minutes for high-K annealing or curing [6]. The main technological challenge is to obtain a well controlled uniform temperature at the wafer surface. So the perfect knowledge of radiative heat emitted by the infrared lamps is necessary. The infrared lamps are usually arranged in banks in the furnace of RTP equipments (Fig. 1b). For information, in a cold wall reactor, the wafer is placed in a chamber and the wall is kept cooled by means of a water flow. A quartz window separates the chamber from the furnace. The radiative heat is transported from the lamps to the wafer through the quartz window and by reflections on the chamber wall. A controller, commonly of Proportional Integral Derivate (PID) type, connected to a pyrometer fixes the input lamp power to respect the setpoint wafer temperature.

Halogen infrared lamps consist of a tungsten filament in a middle of a quartz bulb (Fig. 1a). The latter is filled with nitrogen under around 4 bar of over pressure to reduce the tungsten filament evaporation. Halogen gases with Iodine (I), Bromine (Br), Chlorine (Cl) or Fluorine (F) are added. The created halogen cycle helps tungsten redeposition on the filament. By this method, the lamp lifetime and lamp brightness are increased. Then, the tungsten filament and the electrical power to apply can both remain stable. The lamp bases containing the connectors must be kept under 600 K. Consequently, pulsed air is flowed on the lamp bases during process.

The RTP systems were modelled in different ways. The realized models tend to be more and more accurate to best follow the trends of microelectronic manufacturing requirements.

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