## **Accepted Manuscript**

A pattern recognition approach to transistor array parameter variance

Luciano da F. Costa, Filipi N. Silva, Cesar H. Comin

PII:	\$0378-4371(18)30087-6
DOI:	https://doi.org/10.1016/j.physa.2018.02.011
Reference:	PHYSA 19131
To appear in:	Physica A
Received date :	3 September 2017
Revised date :	22 November 2017



Please cite this article as: L.da F. Costa, F.N. Silva, C.H. Comin, A pattern recognition approach to transistor array parameter variance, *Physica A* (2018), https://doi.org/10.1016/j.physa.2018.02.011

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

Highlights

1. A systematic, quantitative analysis of the properties of bipolar transistors in a same integrated circuit (IC) is presented.

2. It is shown that transistors from the same IC have, on average, 1/20 of the variability in their parameters than transistors from different ICs.

3. The relative variation among devices from different ICs can be large enough to justify customized design for more critical applications.

## دريافت فورى 🛶 متن كامل مقاله

- امکان دانلود نسخه تمام متن مقالات انگلیسی
  امکان دانلود نسخه ترجمه شده مقالات
  پذیرش سفارش ترجمه تخصصی
  امکان جستجو در آرشیو جامعی از صدها موضوع و هزاران مقاله
  امکان دانلود رایگان ۲ صفحه اول هر مقاله
  امکان پرداخت اینترنتی با کلیه کارت های عضو شتاب
  دانلود فوری مقاله پس از پرداخت آنلاین
  پشتیبانی کامل خرید با بهره مندی از سیستم هوشمند رهگیری سفارشات
- ISIArticles مرجع مقالات تخصصی ایران