### Accepted Manuscript

Ferroelectric field-effect transistors based on solution-processed electrochemically exfoliated graphene

Jonas Heidler, Sheng Yang, Xinliang Feng, Klaus Müllen, Kamal Asadi

PII:	S0038-1101(17)30707-4
DOI:	https://doi.org/10.1016/j.sse.2018.03.008
Reference:	SSE 7413
To appear in:	Solid-State Electronics
Received Date:	19 September 2017
Revised Date:	14 March 2018
Accepted Date:	28 March 2018



Please cite this article as: Heidler, J., Yang, S., Feng, X., Müllen, K., Asadi, K., Ferroelectric field-effect transistors based on solution-processed electrochemically exfoliated graphene, *Solid-State Electronics* (2018), doi: https://doi.org/10.1016/j.sse.2018.03.008

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

## **ACCEPTED MANUSCRIPT**

## Ferroelectric field-effect transistors based on solution-processed electrochemically exfoliated graphene

Jonas Heidler<sup>1, a</sup>, Sheng Yang<sup>1</sup>, Xinliang Feng<sup>2</sup>, Klaus Müllen<sup>1,3</sup> Kamal Asadi<sup>1,\*</sup>

<sup>1</sup> Max-Planck Institute for Polymer Research, Ackermannweg 10, 55128 Mainz, Germany.

<sup>2</sup> Molecular Functional Materials, Department of Chemistry and Food Chemistry, Technische Universität Dresden, 01062 Dresden, Germany.

<sup>3</sup> University of Mainz, Institute of Physical Chemistry, Duesbergweg 10, 55128, Mainz, Germany.

<sup>a</sup> Present address: Paul Scherrer Institute, OFLC/102, 5232 Villigen, Switzerland

\* E-mail Address: asadi@mpip-mainz.mpg.de

#### Abstract

Memories based on graphene that could be mass produced using low-cost methods have not yet received much attention. Here we demonstrate graphene ferroelectric (dual-gate) field effect transistors. The graphene has been obtained using electrochemical exfoliation of graphite. Field-effect transistors are realized using a monolayer of graphene flakes deposited by the Langmuir-Blodgett protocol. Ferroelectric field effect transistor memories are realized using a random ferroelectric copolymer poly(vinylidenefluoride-co-trifluoroethylene) in a top gated geometry. The memory transistors reveal ambipolar behaviour with both electron and hole accumulation channels. We show that the non-ferroelectric bottom gate can be advantageously used to tune the on/off ratio.

Keywords: Electrochemically exfoliated graphene, ferroelectric, field-effect transistor, memory, graphene

# دريافت فورى 🛶 متن كامل مقاله

- امکان دانلود نسخه تمام متن مقالات انگلیسی
  امکان دانلود نسخه ترجمه شده مقالات
  پذیرش سفارش ترجمه تخصصی
  امکان جستجو در آرشیو جامعی از صدها موضوع و هزاران مقاله
  امکان دانلود رایگان ۲ صفحه اول هر مقاله
  امکان پرداخت اینترنتی با کلیه کارت های عضو شتاب
  دانلود فوری مقاله پس از پرداخت آنلاین
  پشتیبانی کامل خرید با بهره مندی از سیستم هوشمند رهگیری سفارشات
- ISIArticles مرجع مقالات تخصصی ایران