#### Accepted Manuscript

Full Length Article

Inkjet-Printed p-type Nickel Oxide Thin-Film Transistor

Hailong Hu, Jingguang Zhu, Maosheng Chen, Tailiang Guo, Fushan Li

PII: S0169-4332(18)30392-1

DOI: https://doi.org/10.1016/j.apsusc.2018.02.049

Reference: APSUSC 38507

To appear in: Applied Surface Science

Received Date: 4 November 2017 Revised Date: 31 January 2018 Accepted Date: 4 February 2018



Please cite this article as: H. Hu, J. Zhu, M. Chen, T. Guo, F. Li, Inkjet-Printed p-type Nickel Oxide Thin-Film Transistor, *Applied Surface Science* (2018), doi: https://doi.org/10.1016/j.apsusc.2018.02.049

This is a PDF file of an unedited manuscript that has been accepted for publication. As a service to our customers we are providing this early version of the manuscript. The manuscript will undergo copyediting, typesetting, and review of the resulting proof before it is published in its final form. Please note that during the production process errors may be discovered which could affect the content, and all legal disclaimers that apply to the journal pertain.

### **ACCEPTED MANUSCRIPT**

#### **Inkjet-Printed p-type Nickel Oxide Thin-Film Transistor**

Hailong Hu\*, Jingguang Zhu, Maosheng Chen, Tailiang Guo, Fushan Li\*
College of Physics and Information Engineering, Fuzhou University, Fuzhou
350002, China.

E-mail: huhl@fzu.edu.cn, fushanli@hotmail.com.

High-performance inkjet-printed nickel oxide thin-film transistors (TFTs) with  $Al_2O_3$  high-k dielectric have been fabricated using a sol-gel precursor ink. The "coffee ring" effect during the printing process was facilely restrained by modifying the viscosity of the ink to control the outward capillary flow. The impacts on the device performance was studied in detail in consideration of annealing temperature of the nickel oxide film and the properties of dielectric layer. The optimized switching ability of the device were achieved at an annealing temperature of 280 °C on a 50-nm-thick  $Al_2O_3$  dielectric layer, with a hole mobility of 0.78 cm²/V·s, threshold voltage of -0.6 V and on/off current ratio of  $5.3\times10^4$ . The as-printed p-type oxide TFTs show potential application in low-cost, large-area complementary electronic devices.

#### 1. Introduction

Metal oxide semiconductors (MOS) thin film transistors (TFTs) have attracted tremendous attention for application in the active-matrix

# دريافت فورى ب

## ISIArticles مرجع مقالات تخصصی ایران

- ✔ امكان دانلود نسخه تمام متن مقالات انگليسي
  - ✓ امكان دانلود نسخه ترجمه شده مقالات
    - ✓ پذیرش سفارش ترجمه تخصصی
- ✓ امکان جستجو در آرشیو جامعی از صدها موضوع و هزاران مقاله
  - ✓ امكان دانلود رايگان ۲ صفحه اول هر مقاله
  - ✔ امکان پرداخت اینترنتی با کلیه کارت های عضو شتاب
    - ✓ دانلود فوری مقاله پس از پرداخت آنلاین
- ✓ پشتیبانی کامل خرید با بهره مندی از سیستم هوشمند رهگیری سفارشات